(B) Write ALP for addition of two 8 bit nos. 04 H. and O5H and store result in memory location 1255 H. 6

OR

10. (P) Explain various addressing modes of 8086 with example. 6

(Q) Explain arithmetic instructions of 8086 with suitable example. 6

AP – 552

Third Semester B.C.A. (Part - II) Examination

ELECTRONICS

3 ST 5

P. Pages : 4

Time : Three Hours] [Max. Marks : 60

Note : (1) All questions are compulsory.
(2) All questions carry equal marks.

EITHER

1. (A) Draw and explain timing diagram of memory read cycle. 6

(B) Explain the function of dfferent flags in 8085 with suitable diagram. 6

OR

2. (P) Explain instruction cycle, fetch cycle and execute cycle. 3

(Q) Explain Evolution of microprocessor. 3

(R) Draw block diagram of 8085 µp and explain. 6

EITHER

3. (A) Write an assembly language program for finding minimum of two numbers. 6

(B) Explain various addressing modes of 8085 with example. 6

OR

4. (P) What are the advantages of assembly language over machine language. 3

(Q) Identify the addressing modes of following instructins.

(i) JNZ addr. (ii) MVIA, 05

(iii) ADDB 3

(R) Draw the flow chart and write ALP for multiplication of two 8 bit numbers. 6

EITHER

5. (A) Draw pin diagram of 8255 PPI and explain the function of various pins. 6

(B) Explain memory mapped I/O scheme for address space allocation. 3

(C) Explain various operating modes of 8255 PPI. 3

OR

6. (P) Draw the block diagram of 8255 PPI and explain the function of each block. 6

(Q) Explain control word format of 8255 PPI in I/O mode. 6

EITHER

7. (A) Draw block diagram of 8086 µp and explain. 6

(B) Draw and explain various flags of 8086 µp. 6

OR

8. (P) Explain general purpose registers of 8086 µp. 6

(Q) Explain BIU and EU of 8086 µp. 6

EITHER

9. (A) Explain PUSH and POP instruction of 8086 with suitable example. 6