AT-439

B.C.A. Part—II (Semester-III) Examination ELECTRONICS

Paper—3ST5

Paper—3815					
Tin	ie : T	Three Hours] [N	Maximum Marks : 60		
		B.:— (1) ALL questions are compulsory. (2) All questions carry equal marks. THER			
1.	(Λ)	Draw block diagram of micro computer and explain each block.	6		
	(B)	Explain instruction cycle, machine cycle and T-State.	3		
	(C)	Explain the function of following signals of 8085:			
		(i) IO/\overline{M}			
		(ii) READY			
		(iii) ALE.	3		
	OR	t ·			
2.	(P)	Draw block diagram of 8085 and explain the function of each block	. 6		
	(Q)	Explain the function of following pins in 8085:			
		(i) RD			
		(ii) HOLD			
		(iii) RESET			
		(iv) INTA.	4		
	(R)	Draw pin diagram of 8085.	2		
	EIT	THER			
3.	(Δ)	Explain various addressing modes of 8085 with example.	6		
	(B)	Explain stack operation related to PUSH and POP instruction.	6		

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(Contd.)

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OR

4.	(P)	Define:		
		(i) Algorithm		
		(ii) Flowchart.	2	
	(Q)	Write assembly language program for addition of two 8-bit numbers placed at memory location 2500 H and 2501 H.		
	(R)	Classify instructions and explain arithmetic group of instructions with suitable example.	6	
	EIT	THER		
5.	(A)	Explain memory mapped I/O and I/O mapped I/O scheme.	6	
	(B)	Explain different operating modes of 8255 PPI.	6	
	OR			
6.	(P)	Explain BSR control word format with suitable diagram.	6	
	(Q)	Draw the block diagram of 8255 PPI and explain the function of each block.	6	
	EIT	THER		
7.	(A)	Explain BIU and EU units of 8086.	6	
	(B)	Draw and explain various flags of 8086 μp.	6	
	OR			
8.	(P)	Explain general purpose registers of 8086 μp.	6	
	(Q)	Draw the block diagram of 8086 and explain the function of each block.	6	
	EIT	HER		
9.	(A)	Write an ALP for multiplication of two numbers for 8086 and draw flowchart.	6	
	(B)	Explain various addressing modes of 8086 with suitable example.	6	
	OR			
10.	(P)	Explain logical instruction of 8086 with suitable example.	6	
	(Q)	Write an ALP for addition of two 16-bit numbers and draw flowchart.	6	