

B.E. Seventh Semester (Electrical & Electronics Engineering) (CGS)
10401 : Computer Organization : 7 EX 03

P. Pages : 2

Time : Three Hours



AU - 2913

Max. Marks : 80

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1. a) Explain basic input-output operation with the help of diagram. 8
b) Explain the little Endian assignment with example. 6

OR

2. a) What is the difference between computer architecture and computer organization? Give historical perspective from first generation to fourth generation of computer in terms of architectural and organizational relation. 6
b) Explain any four addressing modes with example. 8
3. a) Explain arithmetic operations of single bus organization. 6
b) Write the control sequence for execution of the instruction Add (R3), R1. 7

OR

4. a) Explain microprogrammed control unit with the help of diagram. 6
b) Explain the timing diagram of READ operation. 7
5. a) What is Interrupt? Explain in brief Daisy chain mechanism for handling Interrupt with the help of diagram. 7
b) Draw and explain handshake control timing diagram for data transfer during input and output operation. 6

OR

6. a) Draw and explain the timing diagram for the input transfer on synchronous Bus. 7
b) What is Bus arbitration? Explain in brief Distributed arbitration. 6
7. a) With the help of block diagram explain the internal organization of dynamic memory chip. 7
b) Explain the working of cache memory. What are various replacement algorithm used in cache memory. 7

OR

8. a) Explain the following. 8
i) ROM ii) PROM
iii) EPROM iv) EEPROM
- b) Explain the concept of virtual memory. 6
9. a) Explain in detail design of FAST adder with generate and propagate function. 7
- b) Write the non-restoring division algorithm perform (1000/11). 6

OR

10. a) Explain Booth's algorithm with suitable example. 7
- b) Design an n-bit carry adder. 6
11. a) Differentiate: 8
i) Synchronous & Asynchronous transmission.
ii) Full-duplex and half-duplex links.
- b) Explain working of Video display. 5

OR

12. a) What are output devices? Explain any one in detail. 7
- b) Explain the working of flat panel display. 6

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