B.Sc. (Part-II) Semester-IV Examination ELECTRONICS

COMMUNICATION ELECTRONICS AND 8085 MICROPROCESSOR

Tim	e : T	hree	Ног	ırs]			[Maximum Marks:	80	
Note :— (1)		All	All questions are compulsory.						
		(2)	Dra	w neat diagram	wherever necess	sary.			
1.	(A)			he blanks :		2			
	` ,			stand for					
				R stand for					
		(iii)	The	The width of data bus of 8085 microprocessor is					
				is					
	(B) Che			pose correct alternative for the following:					
			AM is						
			(a)	Amplitude mo	dem	(b)	Amplitude modulation		
			(c)	Frequency mo	dulation	(d)	None.		
		(ii)	Mo	dulation is oppo	osite process of		_•		
			(a)	Demodulation		(b)	Rectification		
			(c)	Multiplexing		(d)	None		
		(iii)	The	ere are si	debands in AM	ĺ.			
			(a)	One		(b)	Two		
			(c)	Three		(d)	Four		
		(iv)	There are flags in 8085.						
			(a)	3		(b)	8		
			(c)	5		(d)	9		
	(C)	Wri	te ar	nswer in one se	ntence:			4	
		(i)	Wh	at is PPM?					
		(ii)	Wh	at is the function	on of SP?				
		(iii)	i) What is fetch cycle?						
		(iv)	Giv	e the addressin	g mode of STA	6500 I	Н.		
	EIT	HE	R						
2.	(A) Draw the block diagram AM transmitter and exp						explain the function of each blocks.	6	
	(B)	Exp	plain the FM theory and frequency spectrum of FM wave.						

	OK									
	(P)	Draw and explain the block diag	ram of superheterodyne receiver.	8						
	(Q)	Explain the need of modulation i	n communication.	4						
	EITI	HER								
3.		Explain the working of photodio 6	de as an optical detector.							
	(B)	Explain different types of optical	fibers.	6						
	OR									
	(P)	Explain total Internal reflection a	and numerical aperture.	6						
		Explain Laser diode as optical so HER	ource in optical communication system.	6						
4.		Explain the classification of puls	e modulation.	6						
		State and explain FDM with the		6						
	OR	,								
	(P)	Explain PAM and PCM.		8						
	(Q)	What is quantization? Explain.		4						
		HER								
5.	(A)	(A) Draw the block diagram of microcomputer and explain the function of each block								
	(B)	Explain the function of followin	g pins in 8085 microprocessor:							
		(i) RD	(ii) WR							
		(iii) IO/m	(iv) HOLD							
		(v) READY	(vi) D_0 to D_7	6						
	OR		· ·							
	(P)	State and explain the general pu	rpose registers of 8085 microprocessor.	4						
	(Q)	Explain various status flags with suitable diagram of 8085 microprocessor.								
		Explain one byte, two byte and HER	three byte instructions with suitable example.	3						
6.			ain different types of addressing modes with suita	ble 6						
	(B)	B) Draw the flow chart and write ALP for addition of two 8-bit numbers using microprocessor.								
	OR									
		•	ruction set of 8085 microprocessor.	6						
	,		ALP for finding of maximum of two numbers.	6						
		HER								
7.			5 PPI & explain the working of each block.	6						
		Explain memory mapped I/O an	d I/O mapped I/O scheme.	6						
	OR		BSR mode with suitable diagram and example.	_						
				6						
	-(O)	Explain various data transfer se	acmes.	- 6						