(P)	Explain simplex, half and full duplex transmission.				
	6				
(Q)	Explain the interfacing of RS 232 with 8051				
	microcontroller with suitable diagram.				
EIT	HER				
(A)	Explain the status register of AVR AT mega 32 A				
` /	microcontroller. 6				
(B)	Explain various power saving modes of AVR AT				
` ,	mega 32 A microcontroller. 6				
OR					
(P)	Explain X-register, Y-register and Z- register used				
٠.	in AT mega 32A.				
(Q)	Explain SRAM data memory map. 6				
	$\mathcal{L}_{\mathcal{A}} = \{ (1, 1, \dots, n) \mid (1, 1, \dots, n) \in \mathcal{A} \mid (1, \dots, n) \in \mathcal{A} \}$				

	(Adva	ancec	l Mic	ropr	ocesso1	c and	l Micro	controller)	_
Γin	ne : Th	ree F	lours	]	•		[Maxim	um Marks	: 80
	Note	e :	(1)	Ques	tion No	). 1 i	s compu	lsory.	
			(2)	Draw	neat d	iagra	m wher	ever necess	ary.
1.	. (A) Fill in the blanks with appropriated word							word:	
		(i)	808	5 mic	roproce	essor	is	pin IC	
		(ii)	PSV	V stan	ds for				
		(iii)	808	6 has	· 	bi	t flag re	gister.	
		(iv)		_	32A con egister.		of	_type of ge	neral •2
	(B)	(B) Choose correct alternative:							
		(i)		6 mic <b>ie</b> s		esso	r have _	oper	ating
			(a)	5 -					
			(b)	2.					
			(c)	4					
			(d)	16	•				

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(Contd.)

(ii)	IC 8255 is used for	1	EITHER
	(a) Connector		A) Explain arithmetic and logical group of instructions
	(b) Interfacing (c) I/O devices		with suitable example.
	(d) Interrupt		B) State addressing mode of the following instructions
(iii)	Full duplex system consists of way communication.		(i) MOV RO, 14H
	(a) One	1	(ii) MOV DPTR, #1234 H
	(b) Two		(iii) DAA
	(c) Three		(iv) MOV@ RO, 85
	(d) None	1 (	OR .
(iv)	Memory of 8086 microprocessor is divided in segments.		P) Draw a flow chart and write a program to find 2's
	(a) Two		complement of the number. Assume that the number
•	(b) Eight		is stored in register R3 of the register bank O. 6
	(c) Four	·	Q) Explain AJUMP, LJUMP and SJUMP instructions
	(d) Sixteen 2	i	of 8051 microcontroller.
(C) An	swer in one sentence: State memory capacity of 8086 microprocessor.	1	EITHER
(b)	List the general purpose register in AVR at mega 32 A.	6. (	A) What is meant by serial and parallel communication?  State the advantages and disadvantages of serial.
(c)	State the addressing mode of instruction	·	communication.
	MOV AL, [0401]	(	B) Explain the working of PCON register with suitable
(d)	What is the use of source Index register? 4		format.
UBS50081	2 (Contd.)	UBS	50081 5 (Contd.)

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	·	
EITHER		
	and explain block diagran	n of 8086
	in general purpose register of 80	)86 μp. 4
OR		
(P) Expla	in the functions of:	•
(i) 1	instruction pointer	
(ii)	Stack pointer	
(iii)	ALU.	6
(A) Repl	ain the concept of memory segmen	ntation in 8086
(Q) Expr micr	oprocessor with its advantages.	6
EITHER		
one	lain the classification of 8086 in example of each.	
(B) Exp	plain the following instructions in	detail:
<b>(i)</b>	MOV AX, [BX]	
(ii)	PUSH CX.	4
OR		
(P) Dr	aw flow chart and write a prog	ram to multiply
16	bit data at AX = 4561 H	
	CX = 1205  H	6
100 50091	3	(Contd.)

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(Q) State the addressing mode of the following instruct	ions :
(i) MOV CX, SI	,
(ii) MOV [0231], 4356	
(iii) ADD CX, DX	
(iv) ADD AX, $[SI + 05]$ .	4
(R) Write the instruction for 8086 microprocessor whe will perform the indicated operation:	
(i) Multiply AL times BL	
(ii) Increment the content of CX by 1.	2
EITHER	
4. (A) What is microcontroller? Differentiable between	en.
microprocessor and microcontroller.	6
(B) Explain flag register of 8051 microcontroller.	6
OR	
(P) Explain special function register (SFR) of 80: microcontroller.	51
•	6
(Q) What is meant by bit addressable and byte addressab register?	le .
	3
(R) State the important features of 8051 microcontrolle	r.
	3