Faculty of Engineering & Technology

M.Sc. (Applied Electronics) Semester-I (C.B.S. - New) Examination

ELECTRONIC DEVICES & CIRCUITS

(15002)

Paper—1 AE 2 Time: Three Hours] [Maximum Marks: 80 INSTRUCTIONS TO CANDIDATES (1) All questions carry marks as indicated. (2) Due credit will be given to neatness and adequate dimensions. Assume suitable data wherever necessary. Illustrate your answers wherever necessary with the help of neat sketches. 1. Draw and explain forward and reverse biased diode characteristic. (b) Explain diode as a clipper. How is it used as wave shaping circuit? OR market a series of the Control of 2. Explain capacitor filter. Derive expression for ripple factor. For the capacitor filter is full wave rectified input of 30 V dc is supplied with load resistance of 1 k Ω , determine value of capacitor for a ripple factor of 0.01. (b) Compare Half wave, Full wave and Bridge rectifier. 6 What is Q point? Explain the stability of the circuit. Derive expression of stability factor. 3. (b) For a base bias circuit determine V_{CE}, I_C. $V_{CC} = 20 \text{ V}, \ \beta = 100, \ R_{B} = 400 \text{ k}\Omega, \ R_{C} = 2 \text{ k}\Omega.$ 7 Explain working principle, construcTO of PEN diode. St

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(Contd.)

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4.	(a)	Determine stability factor of voltage divider bias circuit and Q point coordinates.	
		$R_1 = 10 \text{ k}\Omega, R_2 = 5 \text{ k}\Omega, R_C = 1 \text{ k}\Omega, R_E = 500 \Omega, V_{CC} = 10 \text{ V}.$	7
	(b)	Define and derive hybrid parameter for CE amplifier.	7
5.	(a)	For a given JFET $I_{DSS} = 15$ mA, $V_{gs(off)} = -5$ V. Determine the drain current if $V_{GS} = -1$ V	7.
		Draw and explain transfer characteristic of JFET.	7
	(b)	Compare BJT and FET.	5
		OR	
6.	(a)	Draw and explain drain and transfer characteristics of MOSFET.	7
	(b)	Explain CMOS.	,
7.	(a)	Explain transformer coupled amplifier. State the advantages. Explain freq. response plot. 7	1
	(b)	Explain direct coupled amplifier. Derive the input, output resistance and gain expression.	
		Tracepase to a state and a second of the sec	1
		OR	
8.	(a)	Explain Bootstrap Emitter follower.	
	(b)	Explain common source FET amplifier.	ĺ
9.	(a)	Prove that power efficiency of class B amplifier is 78.5%.	ľ
	(b)	Explain complementary symmetry push pull power amplifier.	
		OR	
10.	(a)	Explain Colpitt's oscillator in a transistor Colpitt's oscillator $C_1 = .001 \mu F$, $C_2 = 0.01 \mu F$ and	1
	4.	L = 5 μ H. Determine frequency and gain for oscillation.	
11		Explain Wein bridge oscillator.	
11.	3.6	Explain how Schottky diode is a fast switching diode. State uses.	
	(b)	Explain tunneling effect. Explain tunnel diode and its applications.	
10	(-)	OR	
12.		Explain working principle, construction of PIN diode. State its uses.	
		Explain photo–transistor with its characteristics.	
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