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M.Sc. (Applied Electronics) Third Semester (New) (CGS) Elective - II: 15038 - 3 AE 4: Embedded System Design

P. Pages: 2 AR - 2544 Time: Three Hours Max. Marks: 80 Notes: 1. Due credit will be given to neatness and adequate dimensions. 2. Assume suitable data wherever necessary. 3. Illustrate your answers necessary with the help of neat sketches. Explain various hardware components of an embedded system. 7 b) Discuss processor in Embedded system. 7 OR 2. a) Explain functioning of assembler, linker, locator with suitable example. 7 What are software tools used in design of an Embedded system. 7 b) 3. a) What is task? Explain task states. Give task characteristics. Give need of operating system. Define RTOS, and discuss its types. Name some common 7 b) RTOS. OR Discuss interprocess communication using semaphore. 6 4. a) Differentiate between static and dynamic scheduling in multi - processor system. 7 b) 5. Discuss memory management in RTOS. Explain memory selection criteria for different 13 Embedded system using different cast studies. OR 6. What is RTOS? Why it is necessary in Embedded System? 6 a) Give memory management strategy for embedded system. b) Describe features of 8051 and discuss its important pins. 7. a) Draw and explain architecture of 8051 with focus on SFR or draw PSW register. b) OR Explain interrupt structure of 8051. Discuss IE register format. 8. a) Draw and explain PCON & SCON register structure. b)

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9.	a)	Give functioning and features of simulator.	U
٠	b)	Describe -	7
		i) Cross Compiler	
	-	ii) Simulator with reference to embedded system.	
		OR	
10.	a)	Discuss the co-operative round robin scheduling using circular tasks.	6
	b)	Describe round robin cyclic scheduling with time slicing.	7
11.	a)	What is CAN BUS? How does data transmission take place using CAN BUS?	6
	b)	Give meaning of I ² C BUS. Describe serial communication using the I ² C BUS.	7
OR			
12.		Discuss synchronous, ISO - synchronous and Asynchronous communications from serial devices for Embedded systems.	13
